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L Number	Hits	Search Text	DB	Time stamp
	1	("20020116601").PN.	USPAT;	2004/07/28 09:59
			US-PGPUB	
_	1 `	("20020152368").PN.	USPAT;	2004/07/29 09:00
		, , ,	US-PGPUB /	2004/07/00 00 01
_	568652	pipeline processor microprocessor cpu	USPAT;	2004/07/29 09:01
	751170	at a total flush Washing address Warsh	US-PGPUE USPAT;	2004/07/30 11:12
_	751179	stack status flush "return address" push	US-PGPUB	2004/07/30 11:12
	2264	pop "Save state" "instruction bundle" vliw "very long	USPAT;	2004/07/29 09:04
-	2204	instruction bundle virw very long instruction word" "instruction package"	US-PGPUB	2004/01/25 05:04
_	1323490	hard\$1code\$ embedded cache memory stored	USPAT;	2004/07/29 09:09
	1323490	ram rom	US-PGPUB	2004/07/25 05:05
	10001	((interrupt function exception disrruption)	USPAT;	2004/07/29 09:10
	20002	instruction) hear call)	US-PGPUB	
_	33273	((code state context function) near	USPAT;	2004/07/29 09:11
		switch)	US-PGPUB	
_	195881	(pipeline processor microprocessor cpu)	USPAT;	2004/07/29 09:12
		and (stack status Nush "return address"	US-PGPUB	
		push pop "save state")		
-	14126	(pipeline processor microprocessor opu)	USPAT;	2004/07/29 09:12
		and (((code state context function) near	US-PGPUB	
		switch))		0004/07/00 00 40
-	8310	(pipeline processor microprocessor cpu)	USPAT;	2004/07/29 09:12
		and (((interrupt function exception	US-PGPUB	
	7044	disrruption instruction) near (all))	HCDATT.	2004/07/29 09:13
_	7844	((pipeline processor microprocessor cpu) and (stack status flush "retarn address"	USPAT; US-PGPUB	2004/07/29 09:13
		push pop "save state")) and ((Ripeline	US-FGFUB	
		processor microprocessor cpu) and (((code		
		state context function) near switch)))		
_	557	(((pipeline processor microprocessor cpu)	USPAT;	2004/07/29 09:15
	00,	and (stack status flush "return address"	US-PGPUB	
		push pop "save state") and ((pipel\ne		
		processor microprocessor cpu) and ((\code		
		state context function) near switch)) \		
		and ((pipeline progessor microprocessor)		
		cpu) and (((interrupt function exception		
		disrruption instruction) near call)))		
_	660		USPAT;	2004/07/29 09:17
		disrruption instruction) near call)) and	US-PGPUB	
		(((code state context function) near	\	
	11	switch))	USPAT:	2004/07/29 09:15
_	11	disrruption instruction exception disrruption instruction) near call)) near	US-PGPUB	2004/07/25 05:15
		(((code state context function) near	00 10101	
		switch))	\	
	6925	(((interrupt function exception	USPAT;	2004/07/29 09:18
		disrruption instruction) near call)) and	US-PGPUB	
		handl\$/	\	
_	145	(((interrupt function exception	USPAT;	2004/07/29 09:18
		disrruption instruction) near call)) near	US-PGPOB	
		hang1\$	\	
_	8385		USPAT; \	2004/07/29 09:29
		"return address" "save state" "return	US-PGPUB	
		state"	\	0004/07/06 55 ==
-	5853	("stack status" (pipeline near flush)	USPAT;	2004/07/29 16:27
	/	"return address" "save state" "return	US-PGPUB	
		state") and (pipeline processor		
	1223	microprocessor cpu) (("stack status" (pipeline near flush)	USPAT;	2004/07/29 09:35
_	1223	"return address" "save state" "return	US-PGPUB	2000/01/29 09:33
		state") and (pipeline processor	00 10100	
		microprocessor cpu)) and (((interrupt		\
		function exception disrruption		
		instruction) near call))		
			1	1
_	4055	(("stack status" (pipeline near flush)	USPAT:	2004/07/29 09:35
-	4055	(("stack status" (pipeline near flush) "return address" "save state" "return	USPAT; US-PGPUB	2004/07/29 09:35
-	4055	(("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor	1	2004/07/29 09:35

	,			
-	1018	"return address" "save state" "return	USPAT; US-PGPUB	2004/07/29 09:45
	478	state") and (pipeline processor microprocessor cpu)) and (((interrupt function exception disrruption instruction) near call))) and handl\$ (((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and (((interrupt function exception disrruption instruction) near call))) and handl\$) and	USPAT; US-PGPUB	2004/07/29 09:48
-	478	pipeline (((interrupt function exception disrruption instruction) near call)) and ("stack status" (pipeline near flush) "return address" "save state" "return	USPAT; US-PGPUB	2004/07/29 09:50
_	5	state") and hardl\$ and pipeline ((((interrupt function exception disrruption instruction) near call)) near handl\$) and ("stack status" (pipeline near flush) return address" "save state"	USPAT; US-PGPUB	2004/07/29 09:50
_	224	"return state") and pipeline (712/219).CCLS.	USPAT; US-PGPUB	2004/07/29 15:21
_	1	5867701").PN.	USPAT; US-PGPUB	2004/07/29 16:11
-	1/	high-performance and multi-processor and floating and point and unit	EPO	2004/07/29 16:23
-	1827353	WO "9918497" O	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/29 16:27
_	0	wo9918497 🕥	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/29 16:28
_	2	"9918497"	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/29 16:28
-	0 1	"9918497" \hat{V} ("5867701").PN.	IBM_TDB USOCR USPAT;	2004/07/29 16:28 2004/07/30 09:06
	33	("3911406" "4038533" "4050058" "4199811" "4342078" "4363091" "4376976" "4422144" "4542453" "4610000" "4654781" "4751703" "4875160" "4965724" "5003462" "5005118" "5101344" "5129067" "5134701" "5168571" "5202967" "5210758" "5212693" "5233694" "5235686" "5293592" "5307478" "5335344" "5371864" "5452426" "5463745" "5542059" "5546552").PN.	US-PGPUB USPAT	2004/07/30 08:56
	3 4807	5867701.URPN. instruction near (insert instertion add	USPAT USPAT;	2004/07/30 10:37 2004/07/30 09:07
_	334	<pre>pipeline) instruction near (insert instertion)</pre>	US-PGPUB USPAT;	2004/07/30 09:07
_	176614	(interrupt or function call or exception)	US-PGPUB USPAT;	2004/07/30 10:27
_	1247	<pre>and (embed or insert) (interrupt or function call or exception) near (embed or insert)</pre>	US-PGPUB USPAT;	2004/07/30 10:28
_	75	(interrupt or function call or exception) near (embed or insert) and pipeline	US-PGPUB USPAT; US-PGPUB	2004/07/30 10:28
_ _	1 7794	5867701.pn. (embed\$ insert\$) near (code microcod\$	USPAT USPAT;	2004/07/30 10:37 2004/07/30 11:14
	L	instruction opcode)	US-PGPUB	

	819	stack near (status flush "return address"	USPAT;	2004/07/30 11:14
	019	"processor state")	US-PGPUB	
	3411	l =	USPAT;	2004/07/30 11:14
_	3411	instruction opcode)	US-PGPUB	
	94		USPAT;	2004/07/30 11:15
_	94	"processor state")) and ((embed\$ insert\$)	US-PGPUB	2001, 07, 00 11111
		adj (code microcod\$ instruction opcode))	05 13105	
	66	((stack near (status flush "return	USPAT;	2004/07/30 11:17
-	00	address" "processor state")) and ((embed\$	US-PGPUB	2551, 51, 55
		insert\$) adj (code microcod\$ instruction		
		opcode))) and pipeline		
	916		USPAT;	2004/07/30 11:54
_	916	Ingeted near biberine	US-PGPUB	2001,01,00 22101
	75	(insert\$ near pipeline) and stack	USPAT;	2004/07/30 11:18
_	/5	(Threetes Hear biberine) and seack	US-PGPUB	
	73	insert\$ near pipeline and interrupt	USPAT;	2004/07/30 11:55
-	/3	Tuserra hear biberine and interrube	US-PGPUB	2331,31,30 11133
	53	(insert\$ near pipeline and interrupt) not	USPAT;	2004/07/30 12:38
_	33	((insert\$ near pipeline) and stack)	US-PGPUB	2001, 01, 00 12100
	38		USPAT;	2004/07/30 12:38
_	38	((insert\$ near pipeline and interrupt) not ((insert\$ near pipeline) and stack) and	US-PGPUB	2001,0.,00 12:00
			00 1010B	
l .	l .	processor		

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